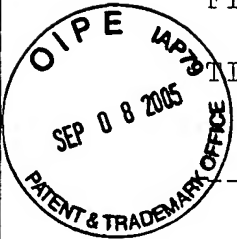


AF
JFW

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICANT: LEE et al. DOCKET NO.: WK2K1080
SERIAL NO: 10/076,701 EXAMINER: GRAYBILL
FILED: 02/13/2002 ART UNIT: 2822
TITLE: A STACKING STRUCTURE FOR SEMICONDUCTOR CHIPS AND
A SEMICONDCUTOR PACKAGE USING IT



Mail Stop: AF
Commissioner For Patents Weiss, Moy & Harris, P.C.
P.O. Box 1450 4204 North Brown Avenue
Alexandria, VA 22313-1450 Scottsdale, Arizona 85251-3989

September 6, 2005

I hereby certify that on the 6th day of September, 2005 this correspondence is being deposited with the U.S. Postal Service as first class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450 Alexandria, VA 22313-1450.



Jeffrey D. Moy

AMENDMENT LETTER

Dear Sir:

This is in response to a Final Office Action dated June 3, 2005, and further in response to an Advisory Action dated August 16, 2005 in regards to the above identified patent application. Applicants respectfully request the entry of the following Amendment Letter, as Applicants believe that Claims 1-19 are now in condition for allowance.

Applicants respectfully submit that Claims 1-19 are in condition for allowance as Shin (US 6,798,049), relied on in the rejection of Claims 1-19 does not constitute prior art under 35 U.S.C. §103 as will be explained in detail below.